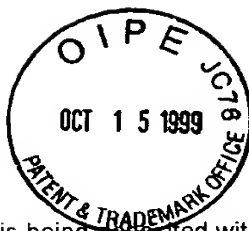


Docket No.: GR 98 P 8041



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By: Mark P. Huttner

Date: 10/12/99

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Huttner et al.
Application No. : 09/313,424
Filed : May 17, 1999
Title : SOI Semiconductor Configuration And Method Of
Fabricating The Same
Art unit : 2811

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

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Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,468,657 (Hsu), dated November 21, 1995;

United States Patent No. 5,596,218 (Soleimani et al.), dated January 21, 1997;

Japanese Patent Abstract No. 02054532 (Takefumi), dated February 23, 1990;

Japanese Patent Abstract No. 03149821 (Juri), dated June 26, 1991;

Japanese Patent Abstract No. 05047726 (Shinichi), dated February 26, 1993;


Japanese Patent Abstract No. 10163218 (Takashi), dated June 19, 1998;

International Patent Application WO 99/17357 (Biebl et al.), dated April 8, 1999.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,


For Applicants

Mark P. Weichselbaum
Reg. No. 43,248

Date: October 12, 1999

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

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